	华半半导体 Huaban Semiconductor
Features	

60V/5.8A Dual N Channel Advanced Power MOSFET

HNS090M06

• Low RDS(on) @VGS=10V

- 5V Logic Level Control
- 100% UIS Tested
- Pb-Free, RoHS Compliant

Applications

- Load Switch
- Switching Circuits
- High Speed line Driver
- •Power management

V(BR)DSS	Rds(on) Typ	I⊳ Max
60V	68mΩ @10V	
	85mΩ @4.5V	5.8A



SOP8

Order Information

Product Package		Marking	Packing		
HNS090M06	SOP8	090M06	3000PCS/Reel		

Absolute Maximum Ratings

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Symbol	Parameter	Rating	Unit				
Common Ratings (TA=25°C Unless Otherwise Noted)							
V _{GS}	Gate-Source Voltage		±20	V			
V _{(BR)DSS}	Drain-Source Breakdown Voltage	60	V				
TJ	Maximum Junction Temperature	150	°C				
Tstg	Storage Temperature Range	-50 to 150	°C				
Mounted on Large Heat Sink							
Ы	Pulse Drain Current Tested(1)	23	А				
lo (Continuous Ducis Consort	T _A =25°C	5.8	A			
	Continuous Drain Current	T _A =70°C	4.5				
P⊳	Maximum Power Dissipation	timum Power Dissipation T _A =25°C 2.1					
EAS	Avalanche energy, single pulsed $\textcircled{2}$	9.6	W				
Rejc	Thermal Resistance Junction-Ambient	60	°C/W				

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Symbol	Parameter	Condition	Min	Тур	Max	Unit		
Static Electrical Characteristics @ TJ = 25°C (unless otherwise stated)								
V(BR)DSS	Drain-Source Breakdown Voltage	Vgs=0V Id=250µA	60			V		
	Zero Gate Voltage Drain Current(T _A =25°C)	Vds=60V, Vgs=0V	-		1	μΑ		
DSS	Zero Gate Voltage Drain Current(T _A =125 $^{\circ}$ C)	Vds=48V, Vgs=0V	-		100	nA		
IGSS	Gate-Body Leakage Current	Vgs=±20V, Vds=0V	-		±100	nA		
Vgs(TH)	Gate Threshold Voltage	Vds=Vgs, Id=250µA	0.8	1.4	2.0	V		
RDS(ON)	Drain-Source On-State Resistance②	Vgs=10, Id=6A		68	85	mΩ		
RDS(ON)	Drain-Source On-State Resistance②	-	85	100	mΩ			
Dynamic Electrical Characteristics @ TJ = 25°C (unless otherwise stated)								
Ciss	Input Capacitance		-	413		pF		
Coss	Output Capacitance	VDS=30V, VGS=0V, f=1MHz		26		pF		
Crss	Reverse Transfer Capacitance			19		pF		
Qg	Total Gate Charge	Vps=30V		10.5		nC		
Qgs	Gate Source Charge	ID=5A,		1.6		nC		
Qgd	Gate Drain Charge	Vgs=10V		2.8		nC		
Switching	Characteristics							
t d(on)	Turn on Delay Time	V 60V		10		ns		
tr	Turn on Rise Time	VDD=30V, ID=1A,	-	29		ns		
t d(off)	Turn Off Delay Time	Rg=3.3Ω, Vgs=10V	-	23		ns		
tr	Turn Off Fall Time			38		ns		
Source Drain Diode Characteristics								
SD	Source drain current(Body Diode)	Ta =25 ℃	-		2	А		
Vsd	Forward on voltage2	Tj=25℃, Isd=2A, Vgs=0V	-	0.84	1.2	V		
Notes:	1	1	1	1	I	I		

Notes:

1 Pulse width limited by maximum allowable junction temperature





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Typical Characteristics





Fig7. Typical Capacitance Vs. Drain-Source Voltage

Fig8. Typical Gate Charge Vs. Gate-Source Voltage



SOP8 Mechanical Data











DIMENSIONS (unit : mm)

Symbol	Min	Тур	Max	Symbol	Min	Тур	Мах
А		1.75		A ₁	0.10	0.18	0.25
A ₂	1.25	1.35	1.45	A 3		0.25	
bp	0.36	0.42	0.49	с	0.19	0.22	0.25
D	4.80	4.92	5.00	E	3.80	3.90	4.00
e		1.27		HE	5.80	5.98	6.20
L		1.05		Lp	0.40	0.68	1.00
Q	0.60	0.65	0.70	v		0.25	
w		0.25		У		0.10	
Z	0.30	0.50	0.70	θ	0°		8°



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